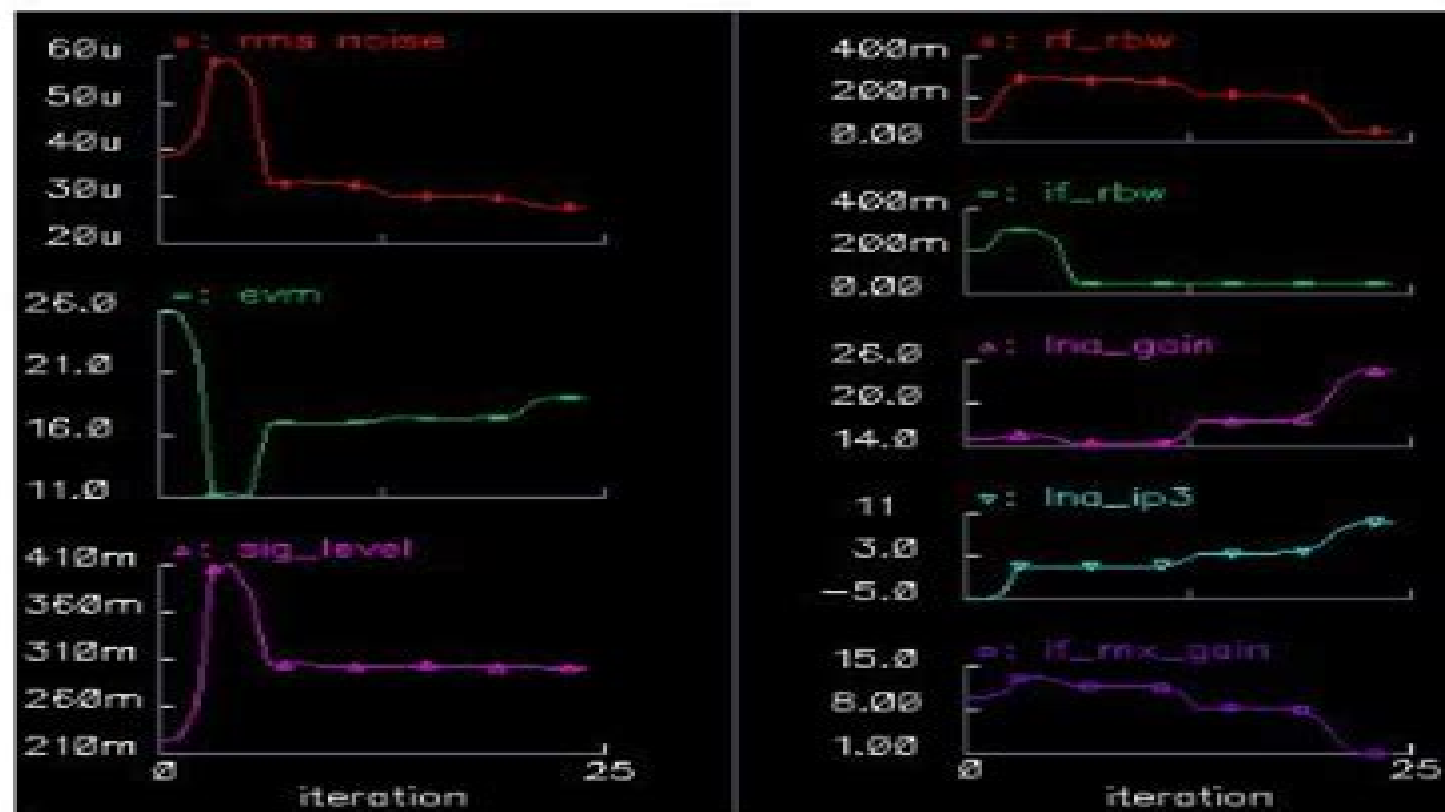


1. If you have time to run the optimization for another 12 iterations you should see the results in [Figure 8-16](#) on page 653.

Figure 8-16 Circuit Optimizer results after 24 iterations



Virtuoso Spectre Circuit Simulator User Guide

Raffaela Di Napoli



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Memristors for Neuromorphic Circuits and Artificial Intelligence Applications Jordi Suñé, 2020-04-09 Artificial Intelligence AI has found many applications in the past decade due to the ever increasing computing power Artificial Neural Networks are inspired in the brain structure and consist in the interconnection of artificial neurons through artificial synapses Training these systems requires huge amounts of data and after the network is trained it can recognize unforeseen data and provide useful information The so called Spiking Neural Networks behave similarly to how the brain functions and are very energy efficient Up to this moment both spiking and conventional neural networks have been implemented in software programs running on conventional computing units However this approach requires high computing power a large physical space and is energy inefficient Thus there is an increasing interest in developing AI tools directly implemented in hardware The first hardware demonstrations have been based on CMOS circuits for neurons and specific communication protocols for synapses However to further increase training speed and energy efficiency while decreasing system size the combination of CMOS neurons with memristor synapses is being explored The memristor is a resistor with memory which behaves similarly to biological synapses This book explores the state of the art of neuromorphic circuits implementing neural networks with memristors for AI applications *Device Circuit Co-Design Issues in FETs* Shubham Tayal, Billel Smaani, Shiromani Balmukund Rahi, Samir Labiod, Zeinab Ramezani, 2023-08-22 This book provides an overview of emerging semiconductor devices and their applications in electronic circuits which form the foundation of electronic devices Device Circuit Co Design Issues in FETs provides readers with a better understanding of the ever growing field of low power electronic devices and their applications in the wireless biosensing and circuit domains The book brings researchers and engineers from various disciplines of the VLSI domain together to tackle the emerging challenges in the field of engineering and applications of advanced low power devices in an effort to improve the performance of these technologies The chapters examine the challenges and scope of FinFET device circuits 3D FETs and advanced FET for circuit applications The book also discusses low power memory design neuromorphic computing and issues related to thermal reliability The authors provide a good understanding of device physics and circuits and discuss transistors based on the new channel dielectric materials and device architectures to achieve low power dissipation and ultra high switching speeds to fulfill the requirements of the semiconductor industry This book is intended for students researchers and professionals in the field of semiconductor devices and nanodevices as well as those working on device circuit co design issues Extreme Environment Electronics John D. Cressler, H. Alan Mantooth, 2017-12-19 Unfriendly to conventional electronic devices circuits and systems extreme environments represent a serious challenge to designers and mission architects The first truly comprehensive guide to this specialized field Extreme Environment Electronics explains the essential aspects of designing and using devices circuits and electronic systems intended to operate in extreme environments including across wide temperature ranges and in radiation

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