

Power Devices

PCB Layout Thermal Design Guide

Thermal design is not only important in designing power circuits, but also an important element in designing the PCB. If an issue occurs after the design is completed, it requires more time and cost to make a modification. Therefore, it is necessary to perform the thermal design from the initial phase of the PCB design as well. This application note provides some key points on how to reduce thermal resistance in designing the PCB.

From here, we change the parameters of each PCB element and check changes in the thermal resistance. The measured value of each thermal resistance is listed for a 1-, 2-, or 4-layer PCB that complies with the JEDEC standard JESD51. Subsequently, after confirming consistency between the measured values as mentioned above and simulations, simulated values are listed for the PCB that are not in accordance with the JEDEC standard.

Since the thermal resistance values depend on the PCB material, layout, parts configuration, chassis shape, surrounding environment, and so on, they may not always be consistent with the values of actual equipment. Therefore, refer to trends of change in the thermal resistance, rather than the absolute values.

Copper foil area

Figure 1 shows the thermal resistance for the 1-layer board with varied copper foil areas. PCB layouts with different copper foil areas are shown in Figure 2. The thermal resistance is decreased as the copper foil area for heat dissipation is increased. However, the effect obtained may not be proportionate to the area if it is expanded beyond a certain extent. Figure 3 is a contour diagram showing that the area of the same temperature is increased as the distance from the heat source is increased. This indicates reduction in the heat dissipation effect.

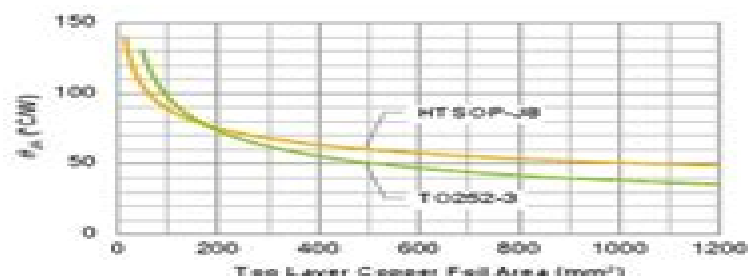


Figure 1. Thermal resistance with varied copper foil areas of the 1-layer board

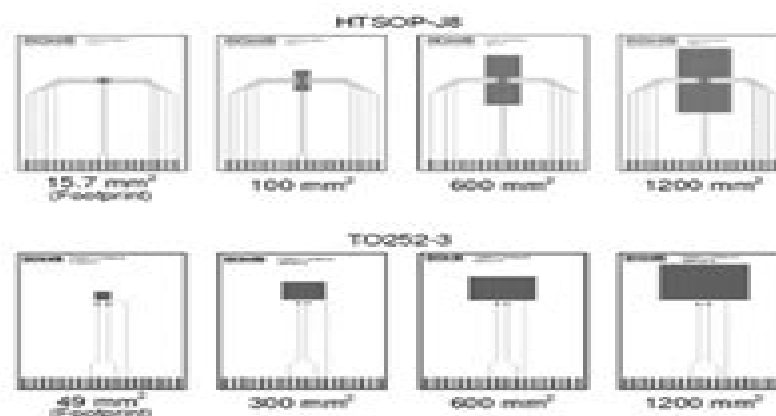
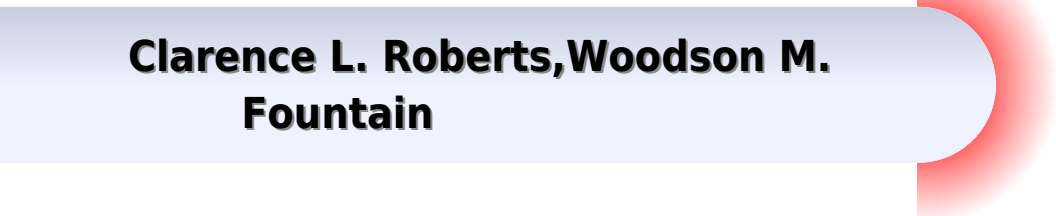


Figure 2. Layout of the 1-layer PCB

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