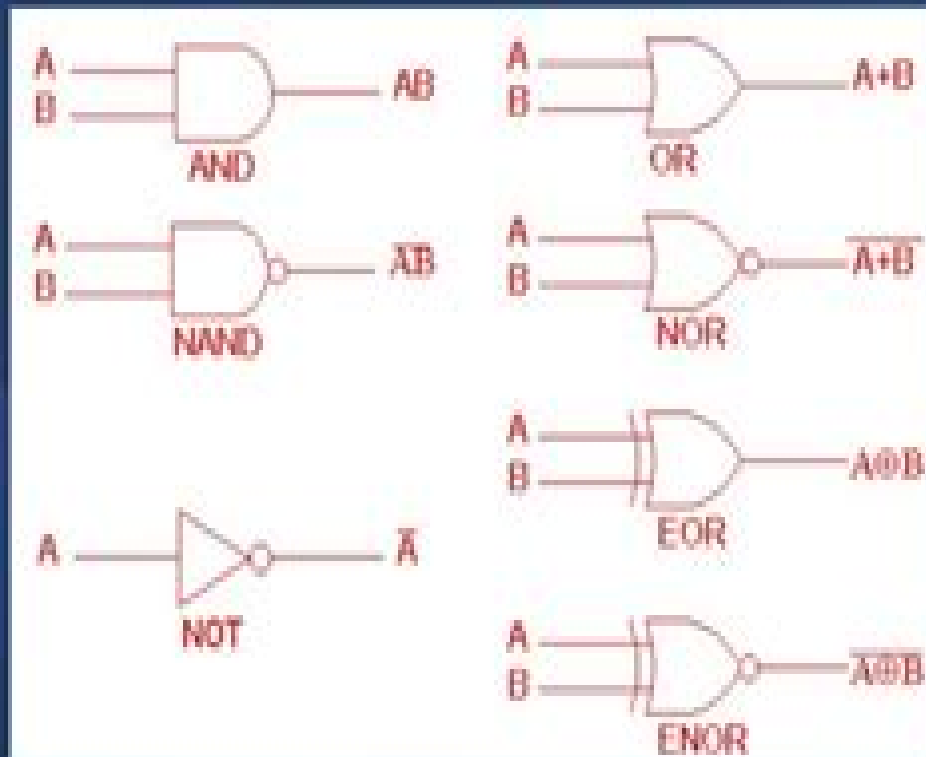


System Verilog Tutorial

Combinational Logic Design



```
// Code your design here
timescale 1ns/1ns
module gates(a, b, and, andn, nand, nor, xor, norx, xnor);
    input logic a, b;
    output reg and, andn, nand, nor, xor, norx, xnor;

    always @(a or b)
    begin
        assign and = (a & b);
        $display("and logical operator");

        assign andn = ~(a & b);
        $display("andn logical operator");

        assign xor = (a ^ b);
        $display("xor logical operator");

        assign nor = ~(a | b);
        $display("nor logical operator");

        assign norx = ((~a) & b) | (a & (~b));
        $display("norx logical operator");

        assign xnor = ((a & b) | (~a & ~b));
        $display("xnor logical operator");
    end
endmodule
```

System Verilog Guide

LL Leslie



System Verilog Guide:

System Verilog Guide Book Review: Unveiling the Power of Words

In a global driven by information and connectivity, the power of words has be evident than ever. They have the ability to inspire, provoke, and ignite change. Such may be the essence of the book **System Verilog Guide**, a literary masterpiece that delves deep in to the significance of words and their affect our lives. Compiled by a renowned author, this captivating work takes readers on a transformative journey, unraveling the secrets and potential behind every word. In this review, we shall explore the book is key themes, examine its writing style, and analyze its overall affect readers.

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