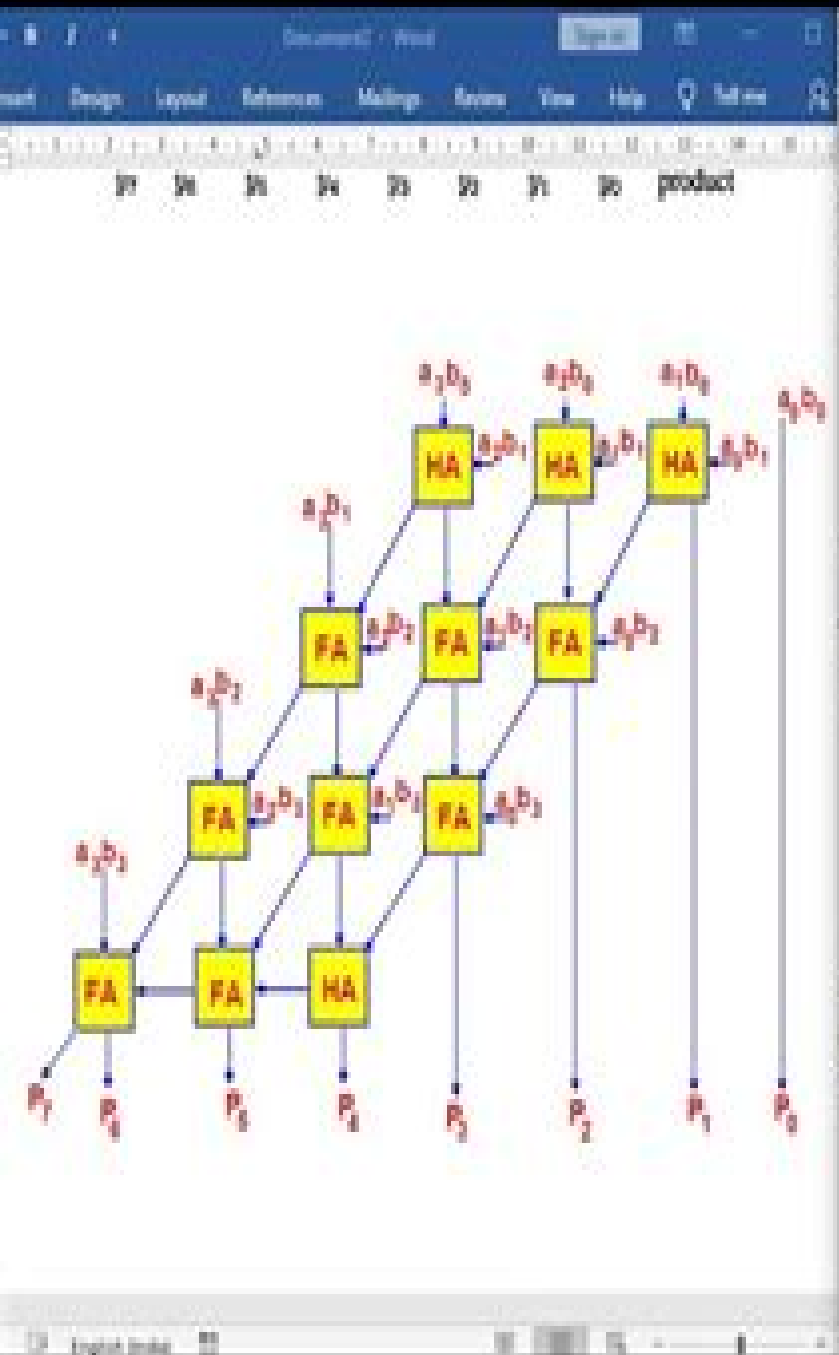




Simulation software interface showing the Project Navigator, Sources in Project, and Process for Source "System_Bn.v" panels.

Project Navigator: Shows the project structure with files like `System_Bn.v`, `System_Bn_1`, `System_Bn_2`, `System_Bn_3`, `System_Bn_4`, `System_Bn_5`, `System_Bn_6`, `System_Bn_7`, `System_Bn_8`, `System_Bn_9`, `System_Bn_10`, `System_Bn_11`, `System_Bn_12`, `System_Bn_13`, `System_Bn_14`, `System_Bn_15`, `System_Bn_16`, `System_Bn_17`, `System_Bn_18`, `System_Bn_19`, `System_Bn_20`, `System_Bn_21`, `System_Bn_22`, `System_Bn_23`, `System_Bn_24`, `System_Bn_25`, `System_Bn_26`, `System_Bn_27`, `System_Bn_28`, `System_Bn_29`, `System_Bn_30`, `System_Bn_31`, `System_Bn_32`, `System_Bn_33`, `System_Bn_34`, `System_Bn_35`, `System_Bn_36`, `System_Bn_37`, `System_Bn_38`, `System_Bn_39`, `System_Bn_40`, `System_Bn_41`, `System_Bn_42`, `System_Bn_43`, `System_Bn_44`, `System_Bn_45`, `System_Bn_46`, `System_Bn_47`, `System_Bn_48`, `System_Bn_49`, `System_Bn_50`, `System_Bn_51`, `System_Bn_52`, `System_Bn_53`, `System_Bn_54`, `System_Bn_55`, `System_Bn_56`, `System_Bn_57`, `System_Bn_58`, `System_Bn_59`, `System_Bn_60`, `System_Bn_61`, `System_Bn_62`, `System_Bn_63`, `System_Bn_64`, `System_Bn_65`, `System_Bn_66`, `System_Bn_67`, `System_Bn_68`, `System_Bn_69`, `System_Bn_70`, `System_Bn_71`, `System_Bn_72`, `System_Bn_73`, `System_Bn_74`, `System_Bn_75`, `System_Bn_76`, `System_Bn_77`, `System_Bn_78`, `System_Bn_79`, `System_Bn_80`, `System_Bn_81`, `System_Bn_82`, `System_Bn_83`, `System_Bn_84`, `System_Bn_85`, `System_Bn_86`, `System_Bn_87`, `System_Bn_88`, `System_Bn_89`, `System_Bn_90`, `System_Bn_91`, `System_Bn_92`, `System_Bn_93`, `System_Bn_94`, `System_Bn_95`, `System_Bn_96`, `System_Bn_97`, `System_Bn_98`, `System_Bn_99`, `System_Bn_100`.

Sources in Project: Shows the source files for the project, including `System_Bn.v`, `System_Bn_1`, `System_Bn_2`, `System_Bn_3`, `System_Bn_4`, `System_Bn_5`, `System_Bn_6`, `System_Bn_7`, `System_Bn_8`, `System_Bn_9`, `System_Bn_10`, `System_Bn_11`, `System_Bn_12`, `System_Bn_13`, `System_Bn_14`, `System_Bn_15`, `System_Bn_16`, `System_Bn_17`, `System_Bn_18`, `System_Bn_19`, `System_Bn_20`, `System_Bn_21`, `System_Bn_22`, `System_Bn_23`, `System_Bn_24`, `System_Bn_25`, `System_Bn_26`, `System_Bn_27`, `System_Bn_28`, `System_Bn_29`, `System_Bn_30`, `System_Bn_31`, `System_Bn_32`, `System_Bn_33`, `System_Bn_34`, `System_Bn_35`, `System_Bn_36`, `System_Bn_37`, `System_Bn_38`, `System_Bn_39`, `System_Bn_40`, `System_Bn_41`, `System_Bn_42`, `System_Bn_43`, `System_Bn_44`, `System_Bn_45`, `System_Bn_46`, `System_Bn_47`, `System_Bn_48`, `System_Bn_49`, `System_Bn_50`, `System_Bn_51`, `System_Bn_52`, `System_Bn_53`, `System_Bn_54`, `System_Bn_55`, `System_Bn_56`, `System_Bn_57`, `System_Bn_58`, `System_Bn_59`, `System_Bn_60`, `System_Bn_61`, `System_Bn_62`, `System_Bn_63`, `System_Bn_64`, `System_Bn_65`, `System_Bn_66`, `System_Bn_67`, `System_Bn_68`, `System_Bn_69`, `System_Bn_70`, `System_Bn_71`, `System_Bn_72`, `System_Bn_73`, `System_Bn_74`, `System_Bn_75`, `System_Bn_76`, `System_Bn_77`, `System_Bn_78`, `System_Bn_79`, `System_Bn_80`, `System_Bn_81`, `System_Bn_82`, `System_Bn_83`, `System_Bn_84`, `System_Bn_85`, `System_Bn_86`, `System_Bn_87`, `System_Bn_88`, `System_Bn_89`, `System_Bn_90`, `System_Bn_91`, `System_Bn_92`, `System_Bn_93`, `System_Bn_94`, `System_Bn_95`, `System_Bn_96`, `System_Bn_97`, `System_Bn_98`, `System_Bn_99`, `System_Bn_100`.

Process for Source "System_Bn.v": Shows the process steps for the source file, including `System_Bn.v`, `System_Bn_1`, `System_Bn_2`, `System_Bn_3`, `System_Bn_4`, `System_Bn_5`, `System_Bn_6`, `System_Bn_7`, `System_Bn_8`, `System_Bn_9`, `System_Bn_10`, `System_Bn_11`, `System_Bn_12`, `System_Bn_13`, `System_Bn_14`, `System_Bn_15`, `System_Bn_16`, `System_Bn_17`, `System_Bn_18`, `System_Bn_19`, `System_Bn_20`, `System_Bn_21`, `System_Bn_22`, `System_Bn_23`, `System_Bn_24`, `System_Bn_25`, `System_Bn_26`, `System_Bn_27`, `System_Bn_28`, `System_Bn_29`, `System_Bn_30`, `System_Bn_31`, `System_Bn_32`, `System_Bn_33`, `System_Bn_34`, `System_Bn_35`, `System_Bn_36`, `System_Bn_37`, `System_Bn_38`, `System_Bn_39`, `System_Bn_40`, `System_Bn_41`, `System_Bn_42`, `System_Bn_43`, `System_Bn_44`, `System_Bn_45`, `System_Bn_46`, `System_Bn_47`, `System_Bn_48`, `System_Bn_49`, `System_Bn_50`, `System_Bn_51`, `System_Bn_52`, `System_Bn_53`, `System_Bn_54`, `System_Bn_55`, `System_Bn_56`, `System_Bn_57`, `System_Bn_58`, `System_Bn_59`, `System_Bn_60`, `System_Bn_61`, `System_Bn_62`, `System_Bn_63`, `System_Bn_64`, `System_Bn_65`, `System_Bn_66`, `System_Bn_67`, `System_Bn_68`, `System_Bn_69`, `System_Bn_70`, `System_Bn_71`, `System_Bn_72`, `System_Bn_73`, `System_Bn_74`, `System_Bn_75`, `System_Bn_76`, `System_Bn_77`, `System_Bn_78`, `System_Bn_79`, `System_Bn_80`, `System_Bn_81`, `System_Bn_82`, `System_Bn_83`, `System_Bn_84`, `System_Bn_85`, `System_Bn_86`, `System_Bn_87`, `System_Bn_88`, `System_Bn_89`, `System_Bn_90`, `System_Bn_91`, `System_Bn_92`, `System_Bn_93`, `System_Bn_94`, `System_Bn_95`, `System_Bn_96`, `System_Bn_97`, `System_Bn_98`, `System_Bn_99`, `System_Bn_100`.

Code Editor: Shows the Verilog code for the source file, including the module definition and the logic for the system.

```
module System_Bn (
    input a1, a2, a3, a4, a5, a6, a7, a8, a9, a10, a11, a12, a13, a14, a15, a16, a17, a18, a19, a20, a21, a22, a23, a24, a25, a26, a27, a28, a29, a30, a31, a32, a33, a34, a35, a36, a37, a38, a39, a40, a41, a42, a43, a44, a45, a46, a47, a48, a49, a50, a51, a52, a53, a54, a55, a56, a57, a58, a59, a60, a61, a62, a63, a64, a65, a66, a67, a68, a69, a70, a71, a72, a73, a74, a75, a76, a77, a78, a79, a80, a81, a82, a83, a84, a85, a86, a87, a88, a89, a90, a91, a92, a93, a94, a95, a96, a97, a98, a99, a100,
    input b1, b2, b3, b4, b5, b6, b7, b8, b9, b10, b11, b12, b13, b14, b15, b16, b17, b18, b19, b20, b21, b22, b23, b24, b25, b26, b27, b28, b29, b30, b31, b32, b33, b34, b35, b36, b37, b38, b39, b40, b41, b42, b43, b44, b45, b46, b47, b48, b49, b50, b51, b52, b53, b54, b55, b56, b57, b58, b59, b60, b61, b62, b63, b64, b65, b66, b67, b68, b69, b70, b71, b72, b73, b74, b75, b76, b77, b78, b79, b80, b81, b82, b83, b84, b85, b86, b87, b88, b89, b90, b91, b92, b93, b94, b95, b96, b97, b98, b99, b100,
    output p1, p2, p3, p4, p5, p6, p7, p8, p9, p10, p11, p12, p13, p14, p15, p16, p17, p18, p19, p20, p21, p22, p23, p24, p25, p26, p27, p28, p29, p30, p31, p32, p33, p34, p35, p36, p37, p38, p39, p40, p41, p42, p43, p44, p45, p46, p47, p48, p49, p50, p51, p52, p53, p54, p55, p56, p57, p58, p59, p60, p61, p62, p63, p64, p65, p66, p67, p68, p69, p70, p71, p72, p73, p74, p75, p76, p77, p78, p79, p80, p81, p82, p83, p84, p85, p86, p87, p88, p89, p90, p91, p92, p93, p94, p95, p96, p97, p98, p99, p100
);

// Logic for the system
// ... (Code continues) ...

endmodule
```

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Yijin Wang



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